

Confirmation:

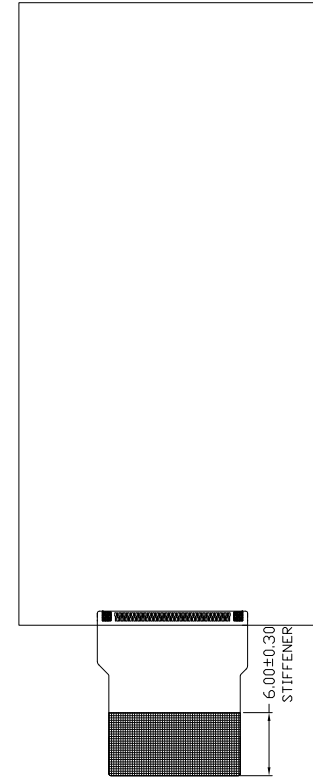
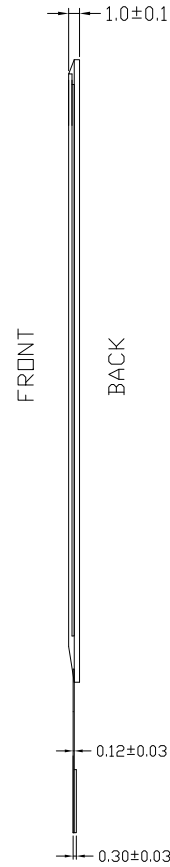
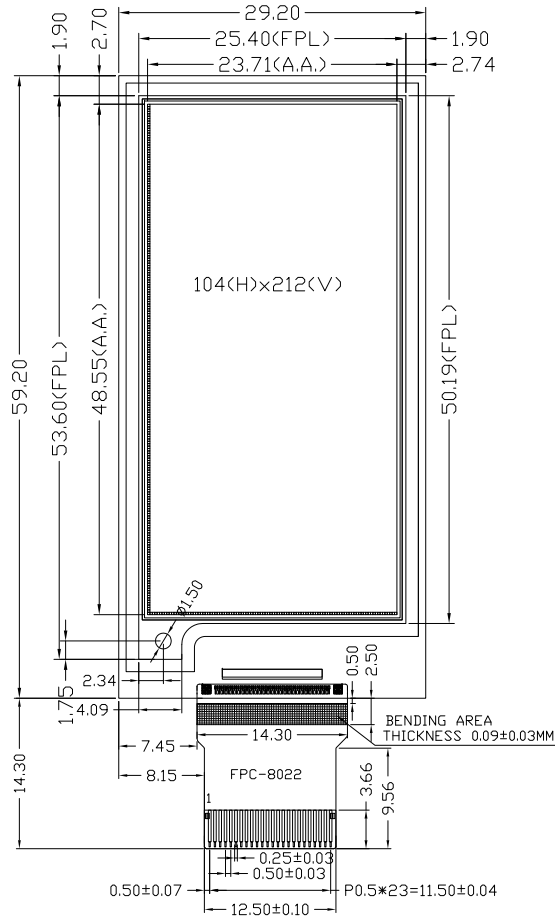
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DATE	REV.	MODIFICATION
2020-07-02	A	FIRST ISSUE

FRONT VIEW

SIDE VIEW

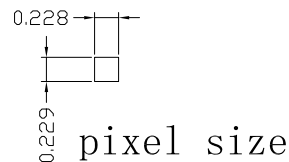
BOTTOM VIEW



PIN	SIGNAL
1	NC
2	GDR
3	RESE
4	NC
5	VDHR
6	TSCL
7	TSDA
8	BS
9	BUSY N
10	RST N
11	DC
12	CS N
13	CLK
14	DATA
15	VDDIO
16	VDD
17	GND
18	VDDD
19	VPP
20	VSH
21	VGH
22	VSL
23	VGL
24	VCOM

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NOTE

- 1 DISPLAY MODULE 2.13" ARRAY FOR EPD
- 2 DRIVER IC:SSD1680
- 3 RESOLUTION:212gateX104source
- 4 PIXEL SIZE:0.229mmX0.228mm



**DKE 东方科脉**

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TOLERANCES UNMARKED ANGLES±5° .X=±0.4mm .XX=±0.20mm	TITLE: EPD	PROJECT: DEPG0213_ S800F42	REV.: A	DATE: 20.07.02	CUST. P/N:
	DWN.	CHK.	APPR.	UNIT: mm	3RD ANGLE: 
CC ZHENG	NN ZHAO	SY ZHAO			PAGE: 1/1

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