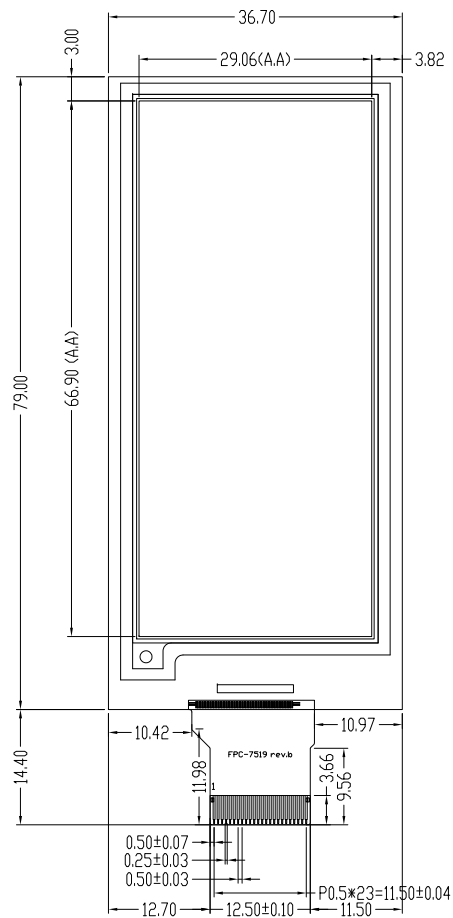


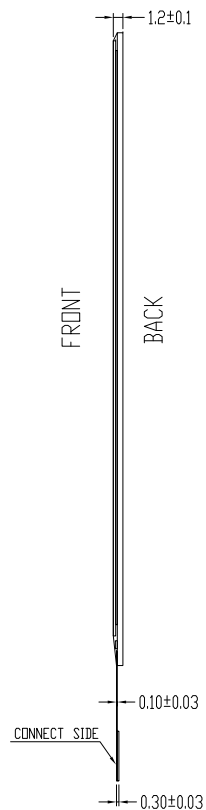
Confirmation:

DATE	REV.	MODIFICATION
2019-10-12	A	FIRST ISSUE

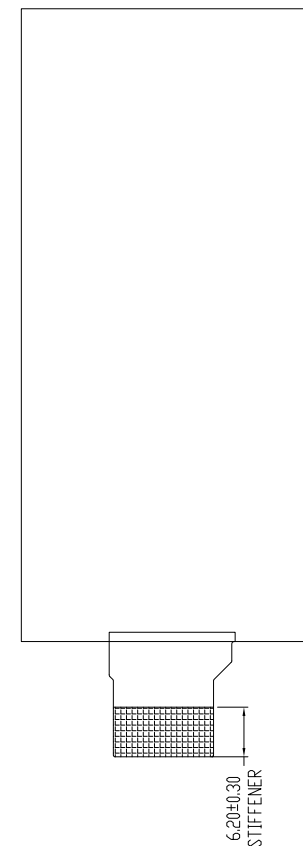
FRONT VIEW



SIDE VIEW



BACK VIEW



PIN	SIGNAL
1	NC
2	GDR
3	RESE
4	NC
5	VSH2
6	TSC1
7	TSDA
8	BS1
9	BUSY
10	RES#
11	D/C#
12	CS#
13	SCL
14	SDA
15	VDDIO
16	VCI
17	VSS
18	VDD
19	VPP
20	VSH1
21	VGH
22	VSL
23	VGL
24	VCOM



**DKE 东方科脉**

<http://www.china-epaper.com/>

NOTE

- 1 DISPLAY MODULE 2.9" ARRAY FOR EPD
- 2 DRIVER IC:SSD1680Z8
- 3 RESOLUTION:296gateX128source
- 4 PIXEL SIZE:0.226mmX0.227mm

TOLERANCES UNMARKED ANGLES±5° .X=±0.4mm .XX=±0.20mm .XXX=±0.200mm	TITLE: EPD	PROJECT: DEPG0290_ S800F6	REV.: A	DATE: 19.10.12	CUST. P/N:
	DWN. XZ FAN	CHK. CC ZHENG	APPR. SY ZHAO	UNIT: mm	3RD ANGLE: 1/1